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#8/Amend (HEAF)
9/13/02 2800
C. Davis
**ATTENTION: BOX AFTER FINAL
RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE REQUESTED
EXAMINING GROUP 2814**

PATENT

Customer No. 22,852

Attorney Docket No. 02887.0141-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Yukihito OOWAKI et al.

Application No.: 09/879,208

Filed: June 13, 2001

For: MIS TRANSISTOR AND METHOD
FOR PRODUCING THE SAME

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

AMENDMENT AFTER FINAL

In reply to the Final Office Action of June 3, 2002, and pursuant to 37 C.F.R.

§ 1.116, Applicants propose that this application be amended as follows:

IN THE CLAIMS:

Please amend claims 14, 18, 22, 27, 32, and 33, and cancel claims 34-39 without
prejudice or disclaimer, as follows:

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14. (Amended) A method for producing a MIS transistor comprising a
semiconductor substrate, source/drain regions formed on the semiconductor substrate,